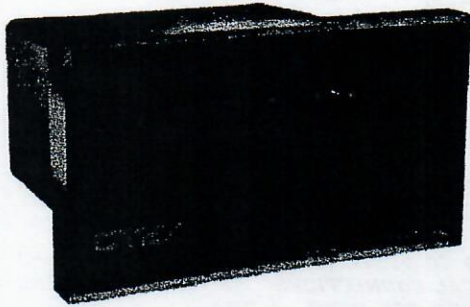


**BCD INPUT, PLC & μ P COMPATIBLE BCD TO
7 SEGMENT SLAVE DISPLAY
TTL or 12V CMOS, MUX or Parallel Input**

**MODEL
329**



4 1/2 DIGIT

Features

- Latched (Memory) Inputs
- Internal/External Clock
- Low Power TTL Inputs or
- CMOS Compatible
- ± 19999 Display
- Bright .6" LED Red Display
- Lamp Test & Display Blank
- Individual Decade
- Latch & Blanking
- Line or Logic Powered



3 1/2 DIGIT

DESCRIPTION: The BCD to seven segment readout is used as a slave display of either Parallel or Serial Multiplexed BCD Data associated with Programmable Logic Controllers (PLC), Digital Panel Meters (DPMs), Digital Counters, Microprocessors, A/D Converters or any other BCD Output Device.

LATCH CLOCK: This pin can be connected to the internal clock for continuous clocking of the latches or it can be synchronized to an external clock. A "0" to "1" transition at this pin serves as a latch enable.

PARALLEL BCD: It is presented to the inputs and its corresponding value is displayed. Internally generated clock pulses scan the display from MSD to LSD.

DISPLAY BLANK: The display can be blanked (except for decimal points, sign & over-range) by holding this pin at Logic "0" (ground); otherwise, leave it open.

MULTIPLEXED BCD: It is accomplished by interconnecting all the 1, 2, 4, 8 Bits of each digit, grounding (Logic 0) all the Latch Inputs and presenting the appropriate BCD information synchronized with the corresponding digit select outputs.

LAMP TEST: Logic 1 (VCC) for normal operation. Logic "0" (ground) for testing of all digit segments.

LATCH: A Logic "0" at latch inputs will enable the 329 to continuously display the BCD Information: Logic "1" at these inputs, it will "latch" the presented BCD information ignoring any changes at the BCD Input.

DECIMAL POINTS: The appropriate decimal point can be energized by connecting this pin to the appropriate digit select (DS) line.

DIGIT SELECTS: Ds1 (LSD) through Ds5 (MSD) are active low signals generated by the internal decoder to select the appropriate digit to be energized and display the decimal equivalent of the latched BCD.

ORDERING INFORMATION (11/1/00)

MODEL **3** **2** **9**

INPUT TYPE

0 TTL
1 CMOS

POWER SUPPLY

0 None
1 120/240VAC

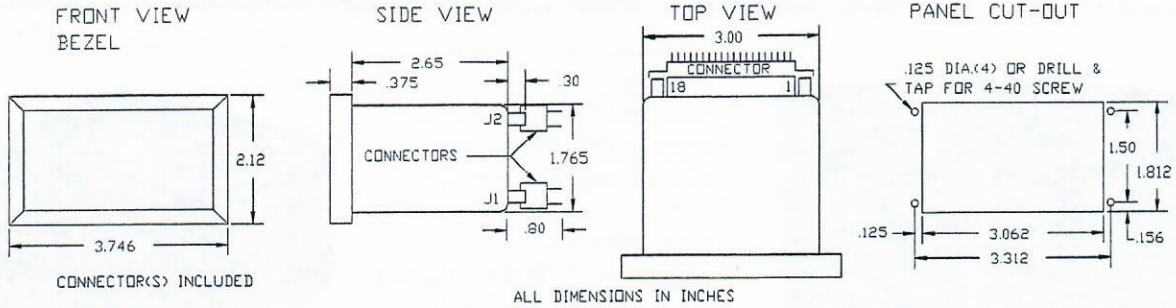
SPECIFICATIONS AT 25°C

Toggle Rate 25MHz/6MHz
 Input Low Power TTL 1 Load/5 PF
 Power TTL 200mA at 5VDC
 Power CMOS 120mA at 12VDC
 Storage Temperature -20° to +70°C
 Operating Temperature -10° to +60°C
 Power Supply (option) 120/240VAC $\pm 10\%$ 47-400Hz Input
 Display 4 1/2 Digit .6" Red LED
 MTBF 100,000 Hours

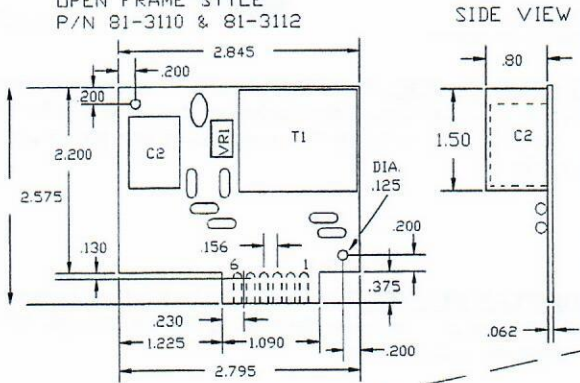
REMOTE DISPLAYS

329 MECHANICALS & TYPICAL CONNECTIONS

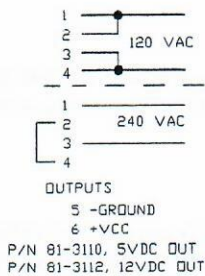
MECHANICALS



MECHANICALS FOR POWER SUPPLIES
INTERNALLY MOUNTED (J2) UPSIDE DOWN
OPEN FRAME STYLE
P/N 81-3110 & 81-3112

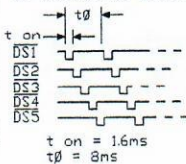


OPEN FRAME
POWER SUPPLY
TYPICAL CONNECTIONS

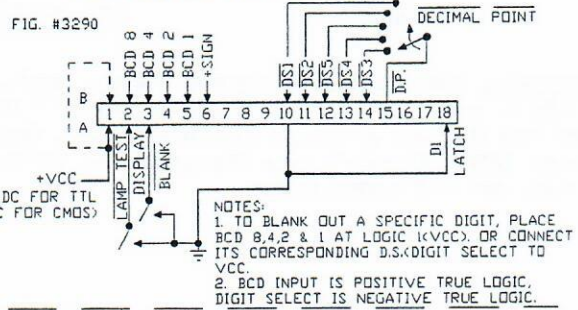


NOTES:
1. TO BLANK OUT SPECIFIC DIGIT, PLACE ITS BCD LINES(8,4,2,1) TO VCC.
2. SYNCHRONIZE THIS 0 TO 1 TRANSITION TO ASSURE ALL DATA TRANSFER.
3. INDIVIDUAL DN LATCH CAN BE PERFORMED.

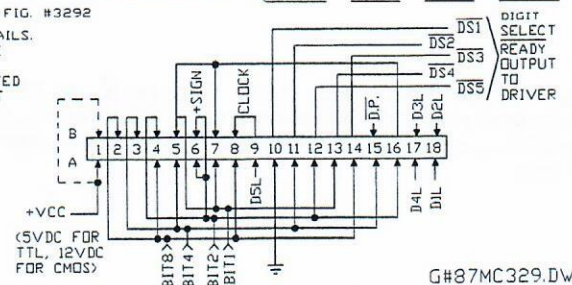
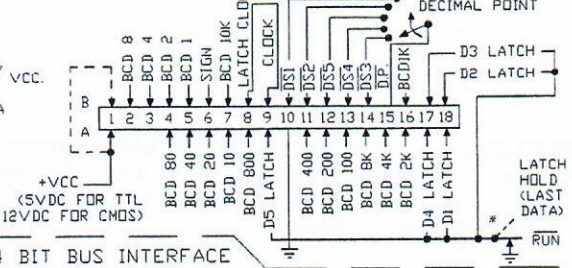
NOTES:
1. DATA IS ACCEPTED DURING LOGIC '0' STATE ONLY.
2. TO LATCH BCD DATA OF ANY DIGIT, CONNECT DNL TO VCC DURING DSN, GROUND FOR CONTINUOUS SAMPLING.
3. SEE FIG. 3291 FOR MORE DETAILS.
4. INTERNAL LATCH USE IS TYPE 74LS173 OR 74C173.
5. +SIGN INPUT CAN BE CONNECTED TO ANY BIT EXCEPT 1, BUT MUST BE CONNECTED WITH DSS.



MOD. 329 BCD TO 7 SEGMENT DISPLAY
TYPICAL CONNECTIONS
MULTIPLEXED BCD INPUT (4 BIT)



PARALLEL BCD INPUT (18 BIT)
FIG. #3291



G#87MC329.DWG

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Section 9 - 2

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